**TRANSMITTAL OF FORMAL DRAWINGS**Docket No.
FIS920030411US1In Application Of: **Hiroyuki Akatsu, et al.**

Serial No.

10/708,340

Filing Date

2/25/04

Confirmation No.

Examiner

Art Unit

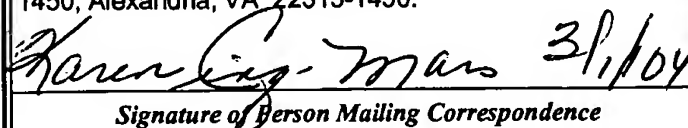
Invention: **STRUCTURE AND METHOD OF SELF-ALIGNED BIPOLAR TRANSISTOR HAVING TAPERED COLLECTOR**

Address to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

10 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).
SignatureH. DANIEL SCHNURMANN
AGENT
REG. NO.: 35,791Dated: MARCH 1, 2004I certify that this document and attached formal drawings are being deposited on **MARCH 1, 2004** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
Signature of Person Mailing Correspondence

KAREN CINQ-MARS

Typed or Printed Name of Person Mailing Correspondence